



REPLACEMENT  
SHEET

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FIG. 1

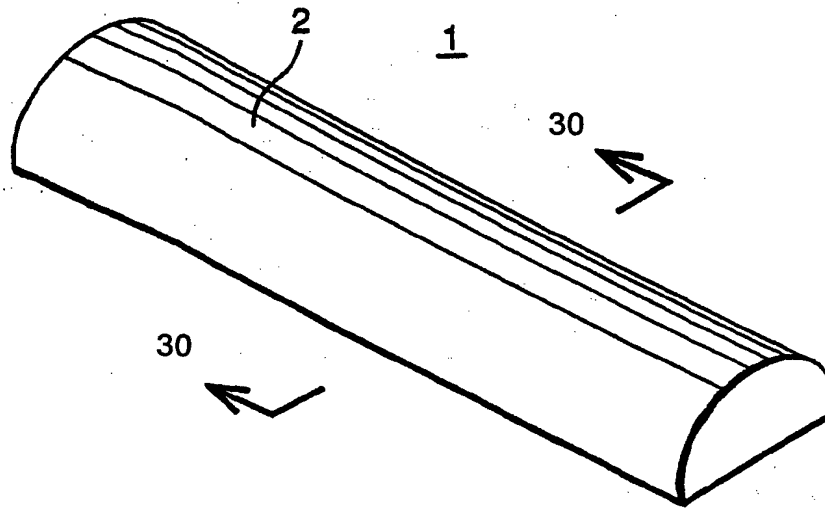


FIG. 2

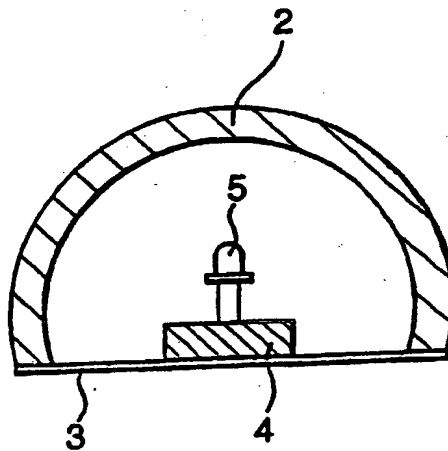
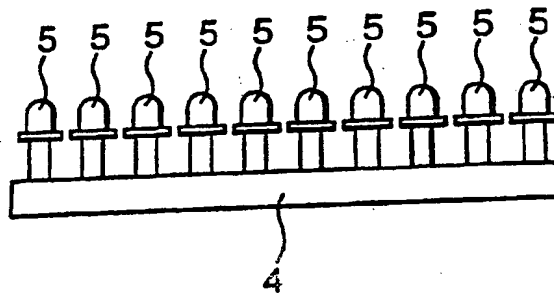


FIG. 3



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graph TD; SI[SERIAL INTERFACE] --- C[CONTROLLER]; C --- PSU1[PSU]; C --- Bus2[2]; Bus2 --- PSU2[PSU]; Bus2 --- PSU3[PSU];
```

The diagram illustrates a circuit for a 2x3 LED display using two 74147 BCD-to-7-segment decoders. The components and connections are as follows:

- 74147 Decoders:** Two 74147 BCD-to-7-segment decoders are used. Each decoder has an **IN** input, three **OUT** outputs, and a **LOAD** input.
- Inputs:**
  - CLOCK:** Connected to the **IN** input of the top decoder.
  - ENABLE:** Connected to the **LOAD** input of the top decoder.
  - DATA IN:** Connected to the **IN** input of the bottom decoder.
- Outputs:**
  - The **OUT** outputs of the top decoder are connected to the **OUT** outputs of the bottom decoder.
  - The **OUT** outputs of the bottom decoder are connected to the **LED'S** (three LEDs).
- Additional Connections:**
  - The **LOAD** input of the bottom decoder is connected to the **LOAD** input of the top decoder.
  - The **DATA OUT** is connected to the **OUT** output of the top decoder.

FIG. 6

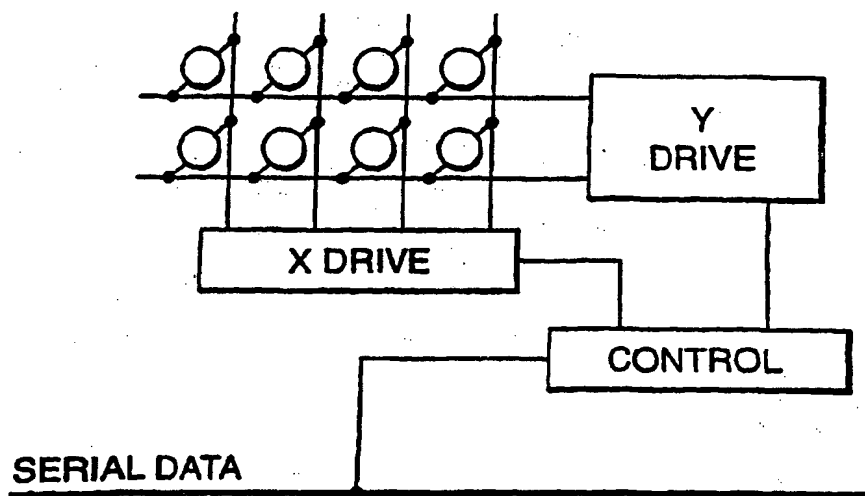
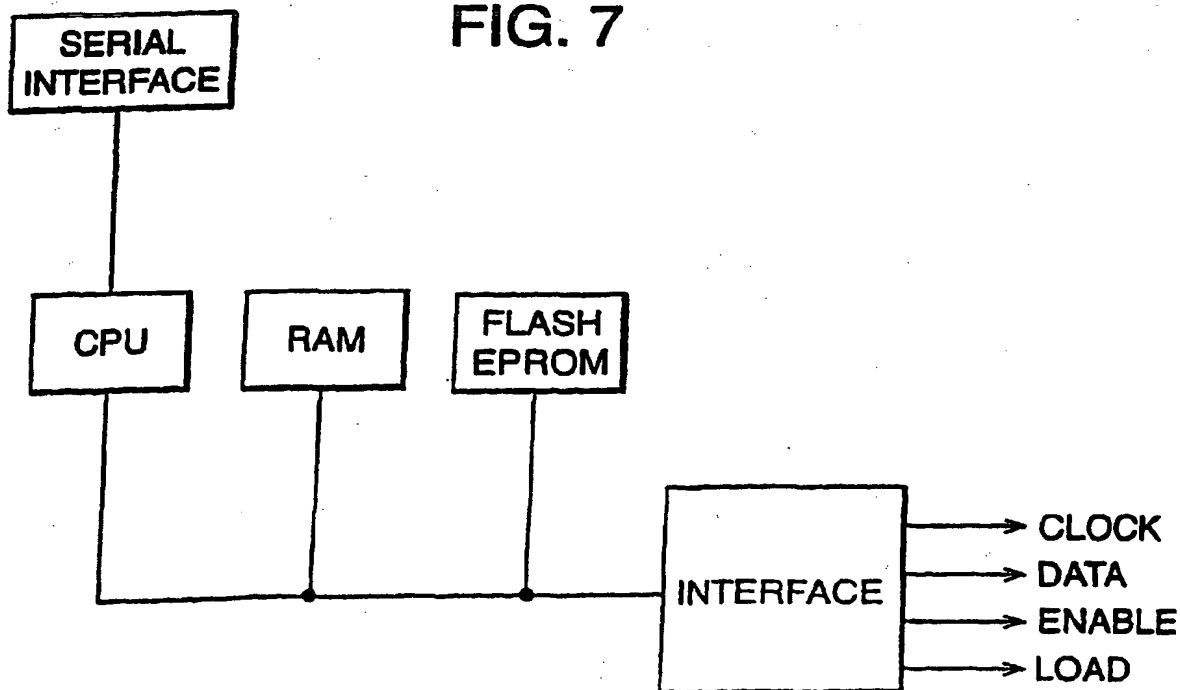
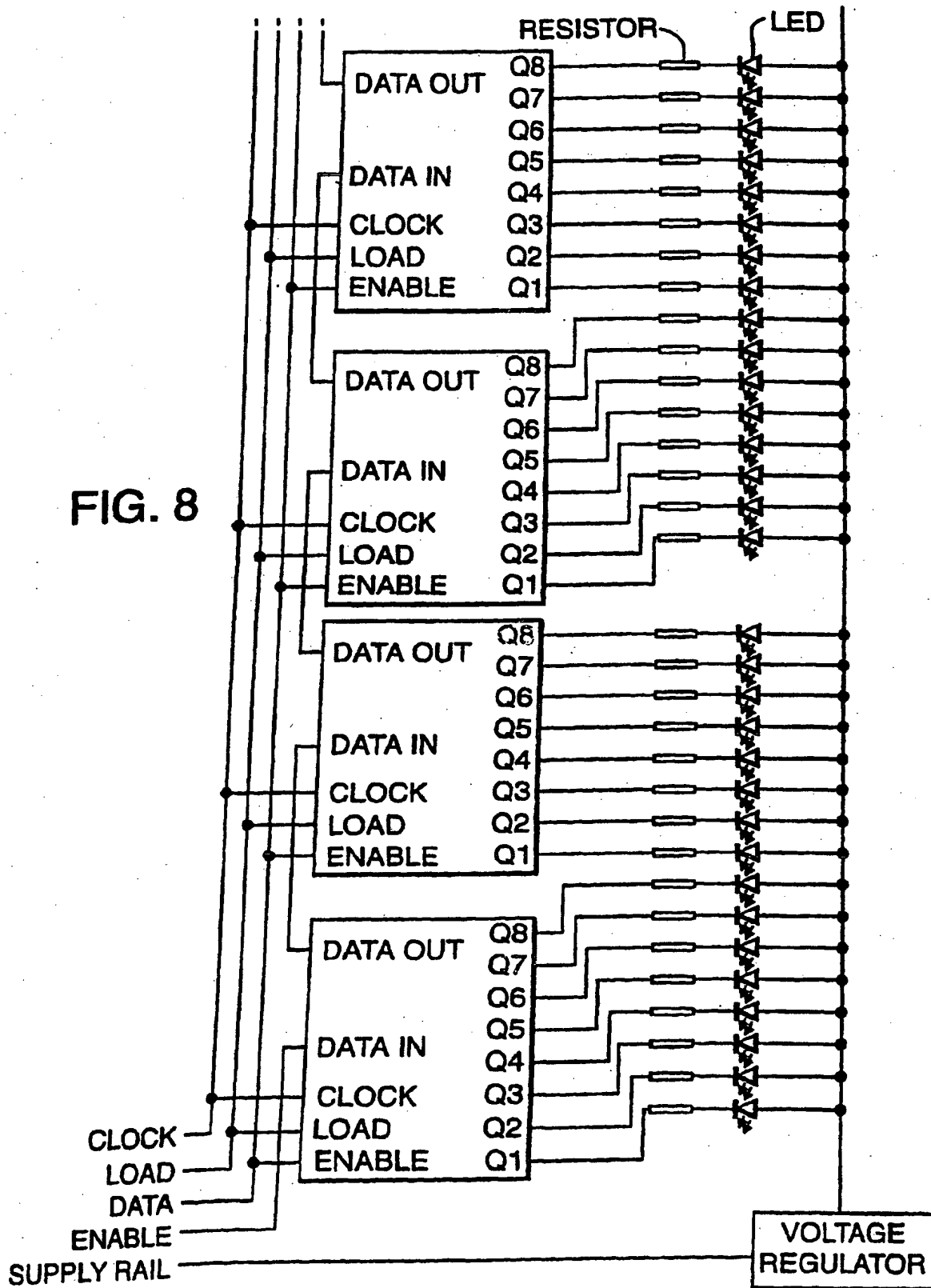


FIG. 7



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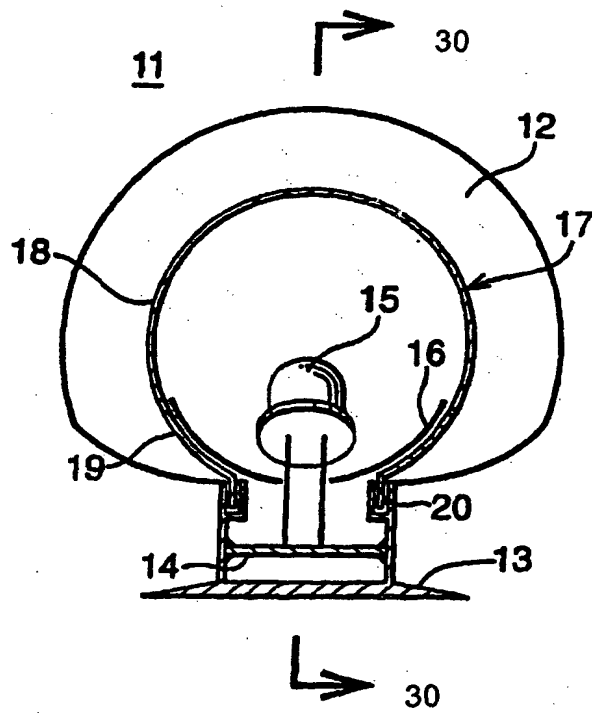


FIG. 9A

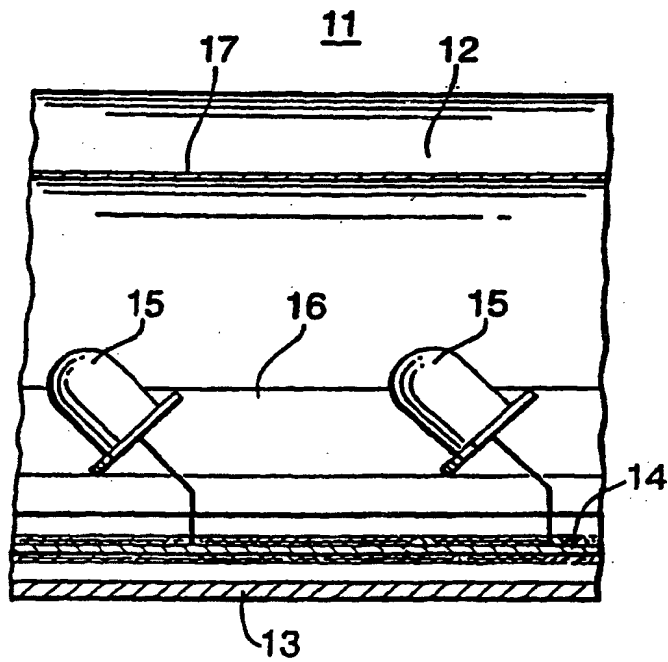


FIG. 9B